ME72 Engineering Design Laboratory

Fall & Winter Terms, 2006-07

(Slowest) TIMELINE

```
        Objectives,                                  Key   Complete
        Requirements,      Key Element          Function   Device
        Functions            Prototype              Test   Prototype           Final
            |  PDR              |                      |   |    Device       Contest
     Rules  |  Mockups,         |                 CDR  |   |   Function            |
   Zero  |   |  3Alts   Analysis |                  |   |   |     Test       Prelim |
     |   |   |     |         |   |                  |   |   |       |             | |
wk 1   2   3   4   5   6   7   8   9  10      11 12  13  14  15  16  17  18  19  20
 |---|---|---|---|---|---|---|---|-##|---|-|##--|---|---|---|---|---|---|---|---|---|
         |    Design         |      Fabricate       | Test/Debug/Refine |Tune |
 |-|-|-|-|-|-|-|-|-|-|-|-|-|-|-|-|-|-##|-|-|-|##|-|-|-|-|-|-|-|-|-|-|-|-|-|-|-|-|-|-|
26 | 3 |10 |17 |24 |31 | 7 |14 |21 |28 | 5 |    9  16  23  30   6  13  20  27   6 |13
  28   5  12  19  26   2   9  16  23  30   7                                        8
Sept      October           November     Dec     January       February      March

 |---|---|---|---|---|---|---|---|-##|---|-|##--|---|---|---|---|---|---|---|---|---|
        88  83  78  73  68  63  58 56  51 49   45  40  35  30  25  20  15  10  5   0
                          Shop Days Remaining
```